

Abstracts

A comparison study of ESD protection for RFICs: performance vs. parasitics

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This paper reports two advanced Electro-Static Discharge (ESD) protection structures suitable for RFICs and a comparison study of influences of ESD parasitic capacitance ($C_{\text{sub ESD}}$) on high-speed circuits. For a 4 GHz ring-oscillator and a low-power high-speed op-amp circuits, it was observed that $C_{\text{sub ESD}}$ may corrupt high-speed performance significantly and new ESD structures can recover the corruption by 80%.

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